Application Data Sheet

Application Information

Application Type::

Regular

Subject Matter::

Utility

Title::

Two-Bit Split-Gate Non-Volatile Memory

Transistor

Attorney Docket Numer::

TSL-105

Request for Early Publication?:: Request for Non-Publication?::

No No

Suggested Drawing Figure::

11

Total Drawing Sheets::

8

Small Entity::

No

Inventor Information

Inventor Authority type::

Inventor

Primary Citizenship Country::

Israel

Status::

Full Capacity

Given Name::

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State or Province of Residence:: Israel

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State or Province::

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Country::

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Assignee Information

Assignee Name:: Tower Semiconductor Ltd.